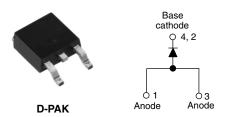


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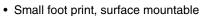
Schottky Rectifier, 3.0 A



PRODUCT SUMMARY				
I _{F(AV)} 3.0 A				
V _R	20 to 40 V			

FEATURES







- · Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS directive 2002/95/EC
- · AEC-Q101 qualified

DESCRIPTION

The MBRD320PbF, MBRD330PbF, MBRD340PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	3.0	Α	
V _{RRM}		20 to 40	V	
I _{FSM}	t _p = 5 μs sine	490	Α	
V _F	3 Apk, T _J = 125 °C	0.49	V	
T _J		- 40 to 150	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	MBRD320PbF	MBRD330PbF	MBRD340PbF	UNITS
Maximum DC reverse voltage	V_{R}	20	30	40	V
Maximum working peak reverse voltage	V_{RWM}	20	30	40	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 133 °C, rectangular waveform		3.0	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	490	Α
non-repetitive surge current	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	75	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 16 \text{mH}$ 8.0		mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		Α	

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MBRD3..PbF Series

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
	V _{FM} ⁽¹⁾	3 A	T _J = 25 °C	0.48	0.6	V
Maximum forward voltage drop		6 A		0.58	0.7	
See fig. 1	VFM\'	3 A	T _J = 125 °C	0.41	0.49	
		6 A		0.55	0.625	
Maximum reverse leakage current	. (1)	T _J = 25 °C	V _B = Rated V _B	0.02	0.2	mA
See fig. 2	T _J = 125 °C	VR = nateu VR	10.7	20	IIIA	
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		189	-	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	-	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R -		10 000	V/µs	

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range	T _J ⁽¹⁾		- 40 to 150	°C	
Maximum storage temperature range	T _{Stg}		- 40 to 175	-0	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	6.0	°C/W	
Maximum thermal resistance, junction to ambient	R _{thJA}		80	C/VV	
Approximate weight			0.3	g	
Approximate weight			0.01	OZ.	
			MBR	D320	
Marking device		Case style D-PAK (similar to TO-252AA)	MBRD330		
			MBR	D340	

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

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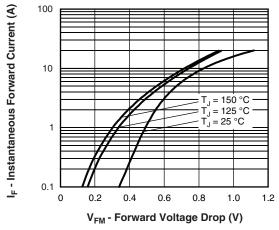


Fig. 1 - Maximum Forward Voltage Drop Characteristics

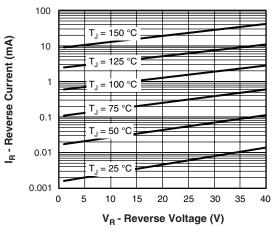


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

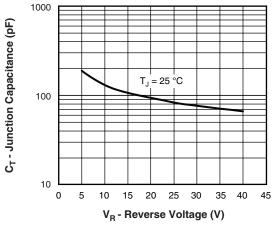


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

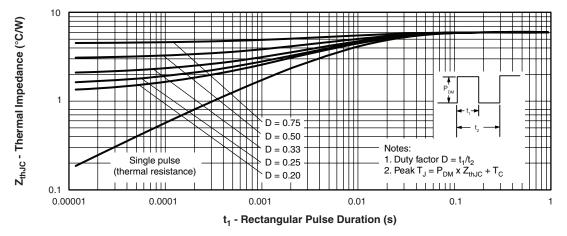
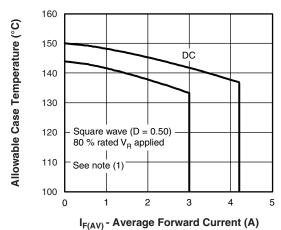


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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Maximum Allawahla Casa Tamparatur

Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

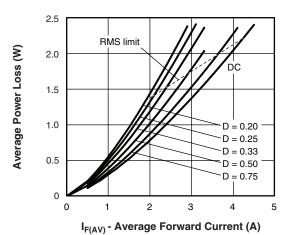


Fig. 6 - Forward Power Loss Characteristics

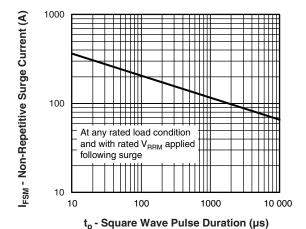


Fig. 7 - Maximum Non-Repetitive Surge Current

Note

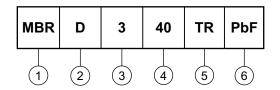


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40 = 40 V

ORDERING INFORMATION TABLE

Device code



Schottky MBR series

D = TO-252AA (D-PAK)

Current rating (3 = 3 A) 20 = 20 V30 = 30 VVoltage ratings -

• None = Tube (50 pieces)

• TR = Tape and reel • TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

PbF = Lead (Pb)-free 6

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95016			
Part marking information	www.vishay.com/doc?95059			
Packaging information	www.vishay.com/doc?95033			

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Vishay

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